

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Yoshiaki Mori, et al. Art Unit: 1756  
Serial No.: 10/026,286 Examiner: Daborah Chacko Davis  
Filed: December 20, 2001 Confirmation No.: 1796  
Title: Pattern Forming Method and Apparatus Used for Semiconductor  
Device, Electric Circuit, Display Module, and Light Emitting Device

**RESPONSE E  
RESPONSE AFTER FINAL REJECTION**

Sir:

In response to the Office Action dated October 18, 2006 (Paper No./Mail Date 20061014), please consider the following remarks: